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1. (Original) A driver circuit comprising:
a plurality of parallel resistors, wherein a total impedance of all said resistors equals an impedance of an associated transmission line; and
a switch adapted to combine said resistors to control an output level of said driver.
2. (Original) The driver circuit in claim 1, wherein said switch selectively connects said resistors to either a voltage high signal or a voltage low signal.
3. (Original) The driver circuit in claim 1, wherein a first set of said switches connect a voltage high signal to a first resistor of said resistors and a second set of switches connect a voltage low signal to a second resistor of said resistors.
4. (Original) The driver circuit in claim 1, wherein said switch comprises a matched pair of opposite type transistors.
5. (Original) The driver circuit in claim 1, further comprising balancing resistors connected to said switch, wherein said balancing resistors are sized to balance the resistance within said driver circuit.
6. (Original) The driver circuit in claim 5, wherein said resistors are said balancing resistors.
7. (Original) The driver circuit in claim 1, further comprising drivers connected to said switches.
8. (Original) A driver circuit connected to a transmission line, said driver circuit comprising:
a plurality of parallel resistors, wherein a total impedance of all said resistors equals an impedance of said transmission line; and

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a plurality of switches, each connected to one of said resistors, said switches being adapted to combine said resistors to control an output level of said driver.

9. (Original) The driver circuit in claim 8, wherein each of said switches selectively connects one of said resistors to either a voltage high signal or a voltage low signal.
10. (Original) The driver circuit in claim 8, wherein a first set of said switches connect a voltage high signal to a first resistor of said resistors and a second set of said switches connect a voltage low signal to a second resistor of said resistors.
11. (Original) The driver circuit in claim 8, wherein each of said switches comprises a matched pair of opposite type transistors.
12. (Original) The driver circuit in claim 8, further comprising balancing resistors connected to said switches, wherein said balancing resistors are sized to balance the resistance within said driver circuit.
13. (Original) The driver circuit in claim 12, wherein said resistors are said balancing resistors.
14. (Original) The driver circuit in claim 8, further comprising drivers connected to said switches.
15. (Original) A driver circuit connected to a transmission line, said driver circuit comprising:
 - a plurality of parallel resistive segments, wherein a total impedance of all said resistive segments equals an impedance of said transmission line,
 - wherein each of said resistive segments includes two resistors and switches connected to

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said resistive segments, and

wherein said switches are adapted to combine said resistors to control an output level of said driver.

16. (Original) The driver circuit in claim 15, wherein said switches selectively connect said resistors to either a voltage high signal or a voltage low signal.

17. (Original) The driver circuit in claim 15, wherein a first set of said switches connect a voltage high signal to a first resistor of said resistors and a second set of switches connect a voltage low signal to a second resistor of said resistors.

18. (Original) The driver circuit in claim 15, wherein each of said switches comprises a matched pair of opposite type transistors.

19. (Original) The driver circuit in claim 15, further comprising balancing resistors connected to said switches, wherein said balancing resistors are sized to balance the resistance within said driver circuit.

20. (Original) The driver circuit in claim 15, further comprising drivers connected to said switches.